Sou. Sushila Danchand Ghodawat Charitable Trust's

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Department of Electronics and Telecommunication Engineering

Report on **VLSI Design** Workshop

Department of E&TC successfully conducted 2 days' workshop on **"VLSI Design"** for BE students on **4th and 5th August** 2018. Resource personnel were from industry background, Mr. Akshay Chinchanikar from *Binalog Electronic design solution*, *Sangli* conducted most of the sessions. Mr. Mahantesh P Mattada worked as faculty coordinator for the workshop.

Workshop aimed to focus on current trends in VLSI, fundamentals of digital design and programming with Verilog HDL. Hands-on sessions were also conducted. Total 42 students were registered for the workshop and participation certificate for all 42 issued by college. Fee per-student for the workshop was very nominal i.e. Rs. 250/-.

Glimpse of the workshop:



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